WEST

Help Logout Interrupt

Main Menu | Search Form | Posting Counts | Show S Numbers | Edit S Numbers | Preferences | Cases

Search Results -

Term	Documents
4.USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	62
(L4).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	62

US Patents Full-Text Database
US Pre-Grant Publication Full-Text Database
JPO Abstracts Database
EPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

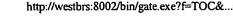
Γ	T.5						 	
l	20	•	-				Refine Search	
L						▼	 	
		Recall Text		Clear	_			

Search History

DATE: Monday, July 22, 2002 Printable Copy Create Case

Set Name side by side		Hit Count	Set Name result set
DB=US	SPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<u>L5</u>	L4	62	<u>L5</u>
DB=US	SPT; PLUR=YES; OP=OR		
<u>L4</u>	L3 and (substrate or single)	62	<u>L4</u>
<u>L3</u>	process\$3 near4 request\$ near5 (speed or rate) near4 memory	87	<u>L3</u>
<u>L2</u>	request\$ near5 (speed or rate) near4 memory	414	<u>L2</u>
<u>L1</u>	request\$ near8 (speed or rate) near4 memory	640	<u>L1</u>

END OF SEARCH HISTORY



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Search Results - Record(s) 1 through 9 of 9 returned.

☐ 1. Document ID: US 6339808 B1

L2: Entry 1 of 9

File: USPT

US-PAT-NO: 6339808

DOCUMENT-IDENTIFIER: US 6339808 B1

TITLE: Address space conversion to retain software compatibility in new

architectures

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Drawi Desc Image

☐ 2. Document ID: US 6253304 B1

L2: Entry 2 of 9

File: USPT

US-PAT-NO: 6253304

DOCUMENT-IDENTIFIER: US 6253304 B1

TITLE: Collation of interrupt control devices

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Drawi Desc Image

☐ 3. Document ID: US 6243817 B1

L2: Entry 3 of 9

File: USPT

US-PAT-NO: 6243817

DOCUMENT-IDENTIFIER: US 6243817 B1

TITLE: Device and method for dynamically reducing power consumption within input

buffers of a bus interface unit

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw Desc Image

☐ 4. Document ID: US 6199134 B1

L2: Entry 4 of 9

File: USPT

US-PAT-NO: 6199134

DOCUMENT-IDENTIFIER: US 6199134 B1

TITLE: Computer system with bridge logic that asserts a system management interrupt signal when an address is made to a trapped address and which also completes the cycle to the target address

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Drawi Desc Image

☐ 5. Document ID: US 6122679 A

L2: Entry 5 of 9

File: USPT

US-PAT-NO: 6122679

DOCUMENT-IDENTIFIER: US 6122679 A

TITLE: Master DMA controller with re-map engine for only spawning programming cycles

to slave DMA controllers which do not match current programming cycle

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw, Desc Image

☐ 6. Document ID: US 6101566 A

L2: Entry 6 of 9

File: USPT

US-PAT-NO: 6101566

DOCUMENT-IDENTIFIER: US 6101566 A

TITLE: Computer system with bridge logic that includes an internal modular expansion

bus and a common target interface for internal target devices

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw Desc Image

7. Document ID: US 6070215 A

L2: Entry 7 of 9

File: USPT

US-PAT-NO: 6070215

DOCUMENT-IDENTIFIER: US 6070215 A

TITLE: Computer system with improved transition to low power operation

Full Title Citation Front Review Classification Date Reference Sequences Attachments Draw. Desc Image

KWIC

∇ 8. Document ID: US 5991833 A

L2: Entry 8 of 9

File: USPT

US-PAT-NO: 5991833

DOCUMENT-IDENTIFIER: US 5991833 A

TITLE: Computer system with bridge logic that reduces interference to CPU cycles during secondary bus transactions

Full Title Citation Front Review Classification Date Reference Sequences Attachments

Drawl Desc | Image |

☐ 9. Document ID: US 5969750 A

L2: Entry 9 of 9

File: USPT

US-PAT-NO: 5969750

DOCUMENT-IDENTIFIER: US 5969750 A

TITLE: Moving picture camera with universal serial bus interface

Full Title Citation Front Review Classification Date Reference Sequences Attachments

Draw Desc Image

KOAC

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Term	Documents
NORTH.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	114528
NORTHS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	10
SOUTH.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	71774
SOUTHS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	10
CPU.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	262252
CPUS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	12433
REQUEST\$3	0
REQUEST.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	232095
REQUESTA.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
REQUESTACK.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
REQUESTB.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	4
((NORTH OR SOUTH) NEAR8 CPU NEAR5 REQUEST\$3).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	9

There are more results than shown above. Click here to view the entire set.

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Search Results - Record(s) 81 through 87 of 87 returned.

☑ 81. Document ID: US 4152764 A

L3: Entry 81 of 87

File: USPT

US-PAT-NO: 4152764

DOCUMENT-IDENTIFIER: US 4152764 A

TITLE: Floating-priority storage control for processors in a multi-processor system

Full Title Citation Front Review Classification Date Reference Sequences Attachments KMC Draw, Desc Image

82. Document ID: US 4141067 A

L3: Entry 82 of 87

File: USPT

US-PAT-NO: 4141067

DOCUMENT-IDENTIFIER: US 4141067 A

TITLE: Multiprocessor system with cache memory

Full Title Citation Front Review Classification Date Reference Sequences Attachments

Draws Desc Image

☐ 83. Document ID: US 4126893 A

L3: Entry 83 of 87

File: USPT

US-PAT-NO: 4126893

DOCUMENT-IDENTIFIER: US 4126893 A

TITLE: Interrupt request controller for data processing system

Full Title Citation Front Review Classification Date Reference Sequences Attachments

Draw Desc Image

MARC

☐ 84. Document ID: US 4080652 A

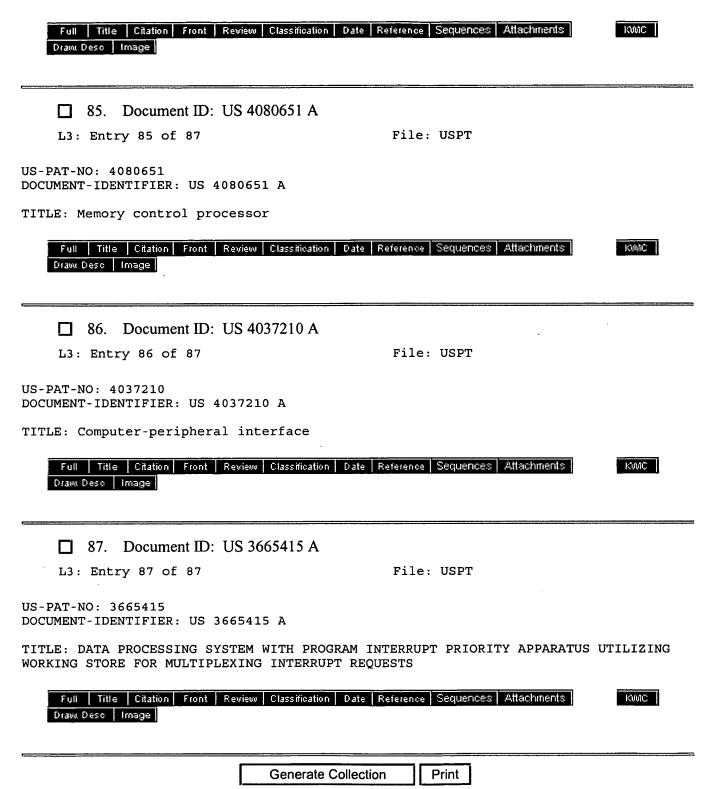
L3: Entry 84 of 87

File: USPT

US-PAT-NO: 4080652

DOCUMENT-IDENTIFIER: US 4080652 A

TITLE: Data processing system





Term	Documents
PROCESS\$3	0
PROCESS.USPT.	1374739
PROCESSA.USPT.	18
PROCESSAB.USPT.	1
PROCESSABI.USPT.	4
PROCESSACH.USPT.	1
PROCESSAED.USPT.	1
PROCESSAL.USPT.	1
PROCESSALL.USPT.	21
PROCESSAND.USPT.	11
PROCESSANI.USPT.	1
(PROCESS\$3 NEAR4 REQUEST\$ NEAR5 (SPEED OR RATE) NEAR4 MEMORY).USPT.	87

There are more results than shown above. Click here to view the entire set.

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Search Results - Record(s) 81 through 87 of 87 returned.

☑ 81. Document ID: US 4152764 A

L3: Entry 81 of 87

File: USPT

US-PAT-NO: 4152764

DOCUMENT-IDENTIFIER: US 4152764 A

TITLE: Floating-priority storage control for processors in a multi-processor system

Full Title Citation Front Review Classification Date Reference Sequences Attachments KMC Draw Desc Image

☐ 82. Document ID: US 4141067 A

L3: Entry 82 of 87

File: USPT

US-PAT-NO: 4141067

DOCUMENT-IDENTIFIER: US 4141067 A

TITLE: Multiprocessor system with cache memory

Full Title Citation Front Review Classification Date Reference Sequences Attachments KMC |
Drawn Desc | Image |

☐ 83. Document ID: US 4126893 A

L3: Entry 83 of 87

File: USPT

US-PAT-NO: 4126893

DOCUMENT-IDENTIFIER: US 4126893 A

TITLE: Interrupt request controller for data processing system

Full Title Citation Front Review Classification Date Reference Sequences Attachments KWIC

Drawl Desc | Image |

☐ 84. Document ID: US 4080652 A

L3: Entry 84 of 87

File: USPT

US-PAT-NO: 4080652

DOCUMENT-IDENTIFIER: US 4080652 A

TITLE: Data processing system

☐ 85. Document ID: US 4080651 A

L3: Entry 85 of 87

File: USPT

US-PAT-NO: 4080651

Drawi Desc Image

DOCUMENT-IDENTIFIER: US 4080651 A

TITLE: Memory control processor

Full Title Citation Front Review Classification Date Reference Sequences Attachments KMC Draw. Desc | Image |

☐ 86. Document ID: US 4037210 A

L3: Entry 86 of 87

File: USPT

US-PAT-NO: 4037210

DOCUMENT-IDENTIFIER: US 4037210 A

TITLE: Computer-peripheral interface



☐ 87. Document ID: US 3665415 A

L3: Entry 87 of 87

File: USPT

US-PAT-NO: 3665415

DOCUMENT-IDENTIFIER: US 3665415 A

TITLE: DATA PROCESSING SYSTEM WITH PROGRAM INTERRUPT PRIORITY APPARATUS UTILIZING

WORKING STORE FOR MULTIPLEXING INTERRUPT REQUESTS

Full Title Citation Front Review Classification Date Reference Sequences Attachments Draw, Desc Image

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Term	Documents
PROCESS\$3	0
PROCESS.USPT.	1374739
PROCESSA.USPT.	18
PROCESSAB.USPT.	1
PROCESSABI.USPT.	4
PROCESSACH.USPT.	1
PROCESSAED.USPT.	1
PROCESSAL.USPT.	1
PROCESSALL.USPT.	21
PROCESSAND.USPT.	11
PROCESSANI.USPT.	1
(PROCESS\$3 NEAR4 REQUEST\$ NEAR5 (SPEED OR RATE) NEAR4 MEMORY).USPT.	87

There are more results than shown above. Click here to view the entire set.

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